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design actual, working
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16/32-bit CISC
microprocessor,
introduced in 1979 by
Motorola
Semiconductor
Products Sector.. The
design implements a
32-bit instruction set,
with 32-bit registers
and a 32-bit internal
data bus. The address
bus is 24-bits and does
not use memory
segmentation, which
made it popular with ...

Motorola 68000 -

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The CPU, and later the whole family, implemented exactly two levels of privilege. ... supervisor bit was stored in the status register, and visible to user programs. A real advantage of this system was that the

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supervisor level had a
separate stack ...
design a memory
interface for a 68000
processor system. CPU
addresses \$000000 -
\$007FFF 15!

MC68000 **Microprocessor** **Hardware**

The Motorola 68000 series (also known as 680x0, m68000, m68k, or 68k) is a family of 32-bit CISC microprocessors. Durin

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g the 1980s and early
1990s, they were
popular in personal
computers and
workstations and were
the primary
competitors of Intel's
x86 microprocessors.
They were most well
known as the
processors powering
the early Apple
Macintosh, the Sharp
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in work at present one
among the reasons for

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this is that we are a
long way from
exhausting the 64 bit
address space itself,
we use it a constant
rate of roughly 2 ...

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