

The Art Of Hardware Architecture Design Methods And Techniques For Digital Circuits

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The Art of Hardware Architecture Design Methods and Techniques for Digital Circuits. Authors: Arora, Mohit Free Preview. Describes techniques to help IP designers get it right the first time, creating designs optimized in terms of power, area and performance; Focuses on practical aspects of chip design and minimizes theory ...

The Art of Hardware Architecture - Design Methods and ...

Mohit Arora is a Senior Systems Engineer with Freescale Semiconductor. Since joining Freescale in 2005, his responsibilities have included developing systems and architecture requirements and leading the development of VoIP-based SOCs, as well as generic microcontroller SOCs.

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The art of hardware architecture 1. Chapter 2Clocks and Resets2.1 IntroductionThe cost of designing ASICs is increasing every year. In addition to the non-recurringengineering (NRE) and mask costs, development costs are increasing due to ASICdesign complexity.

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There are a lot of books out there that purport to talk about design techniques; but when you get right down to the "nitty-gritty", many of them turn out to be largely theoretical in nature. By comparison, The Art of Hardware Architecture is firmly focused on describing and solving real-world problems using tried-and-true techniques".

Art of Hardware Architecture — Mohit Arora

The art of hardware architecture : design methods and techniques for digital circuits. MohitArora. TheArtofHardware. Architecture. DesignMethodsandTechniques. forDigitalCircuits. Springer. Contents. 1 TheWorldofMetastability1.1.1 Introduction 1.1.2TheoryofMetastability1.

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The Art of Hardware Architecture [] : Arora, Mohit [][]: 2011-10 []: 240 []: \$ 145.77 ISBN: 9781461403968 [][]

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The original idea behind The Art of Hardware Architecture was to link my years of experience as a design architect with the extensive research I have conducted. However, achieving the nal shape of this book would not have been possible without many contributions.

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Hardware architecture is the representation of an engineered (or to be engineered) electronic or electromechanical hardware system, and the process and discipline for effectively implementing the design (s) for such a system. It is generally part of a larger integrated system encompassing information, software, and device prototyping.

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